FIG 1

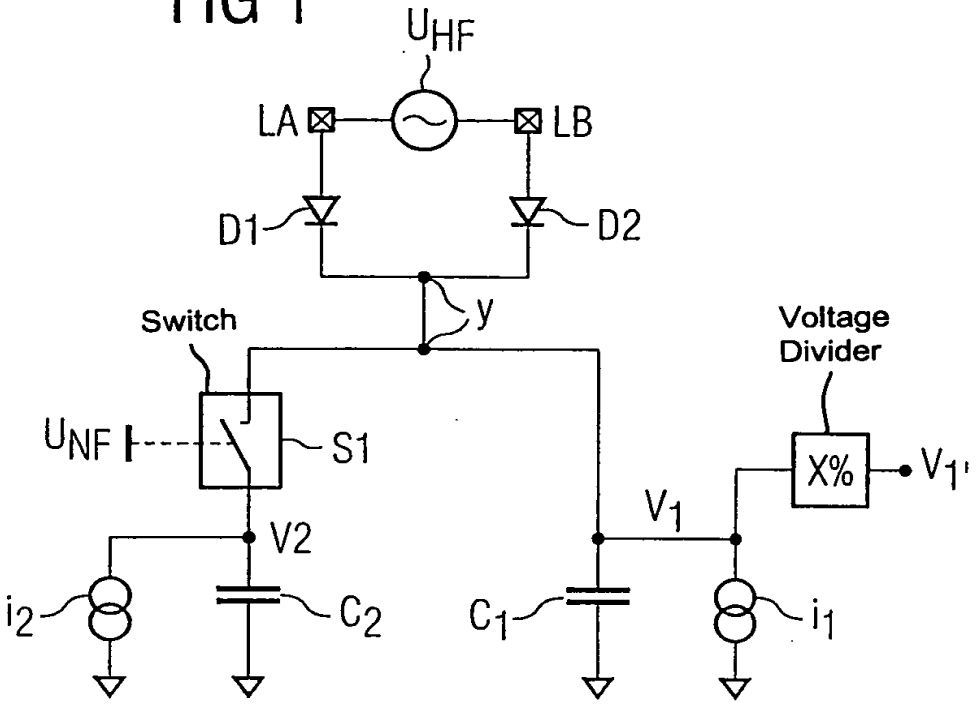

U_{HF}
LA
LB
D1
D2
y
Switch
Voltage Divider
U_{NF}
S1
X%
V_1'
V_1
V2
i_2
C_2
C_1
i_1

FIG 2

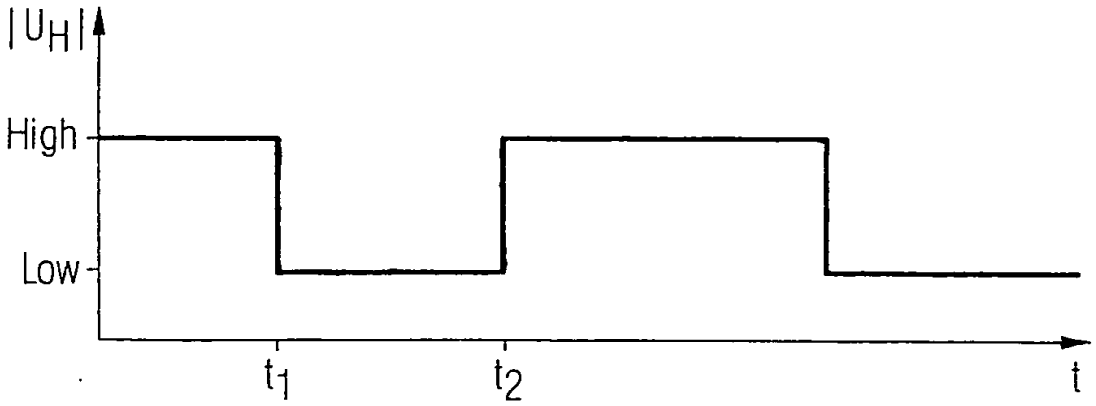

$|U_H|$
High
Low
t_1
t_2
t

FIG 3

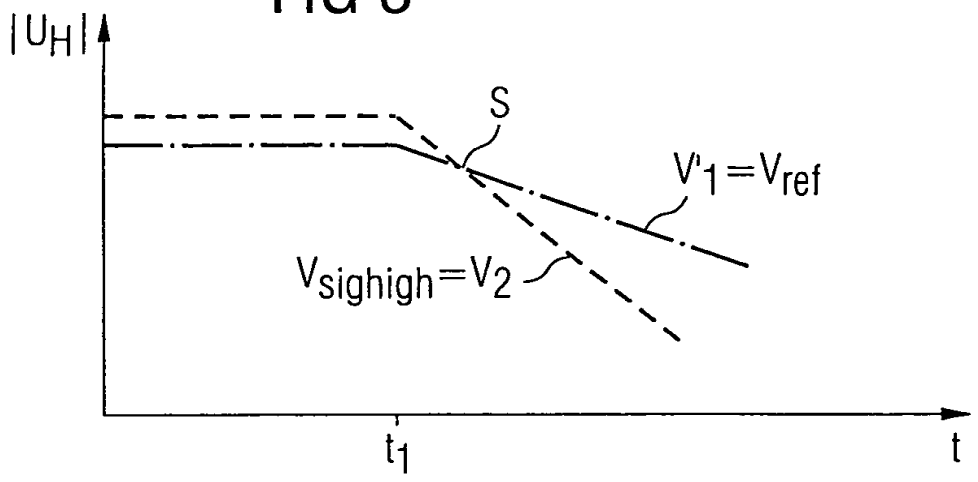

|UH|
S
V'1=Vref
Vsighigh=V2
t1
t

FIG 4

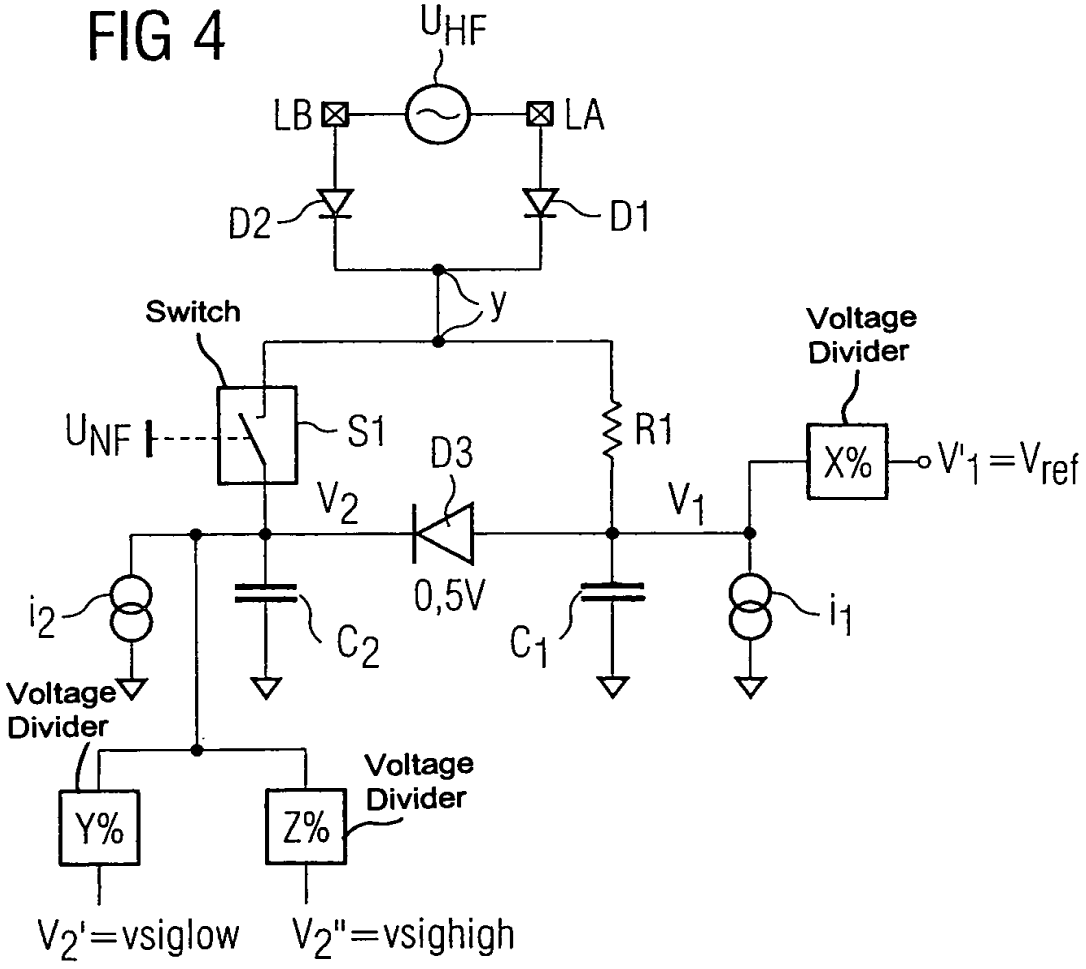

UHF
LB
LA
D2
D1
y
Switch
UNF
S1
R1
D3
V2
V1
Voltage Divider
X%
V'1=Vref
i2
C2
0,5V
C1
i1
Voltage Divider
Y%
Z%
Voltage Divider
V2'=vsiglow
V2"=vsighigh

FIG 5

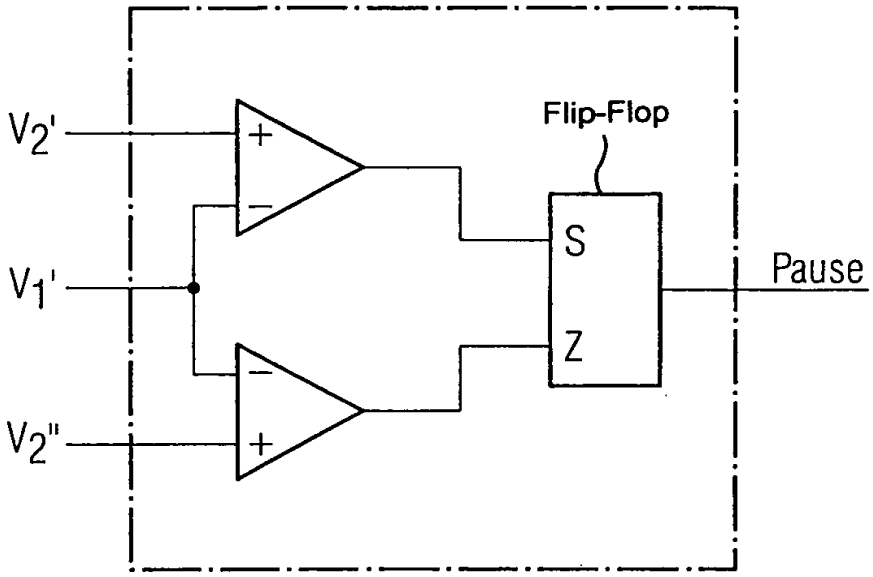

V2'
V1'
V2"
+
−
−
+
Flip-Flop
S
Z
Pause

FIG 6

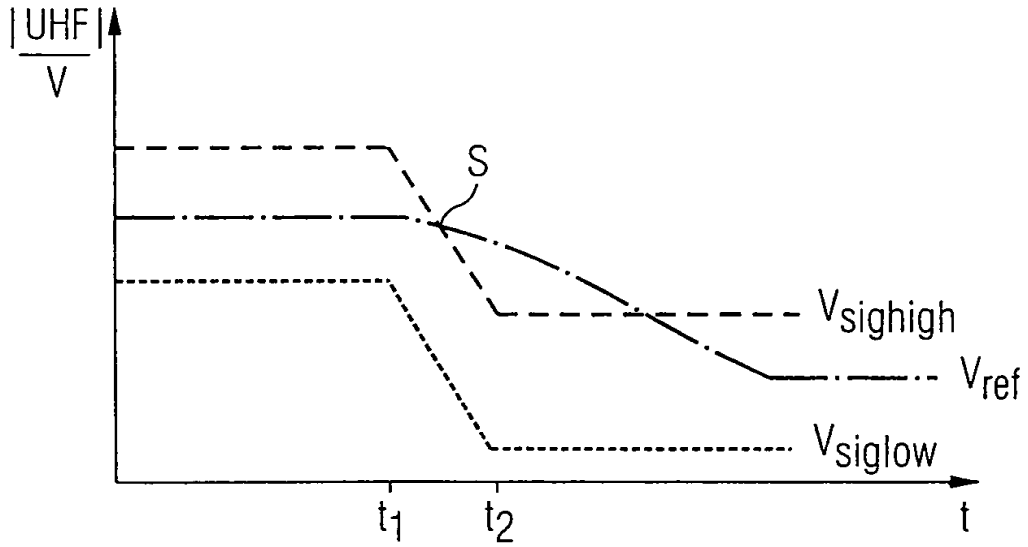
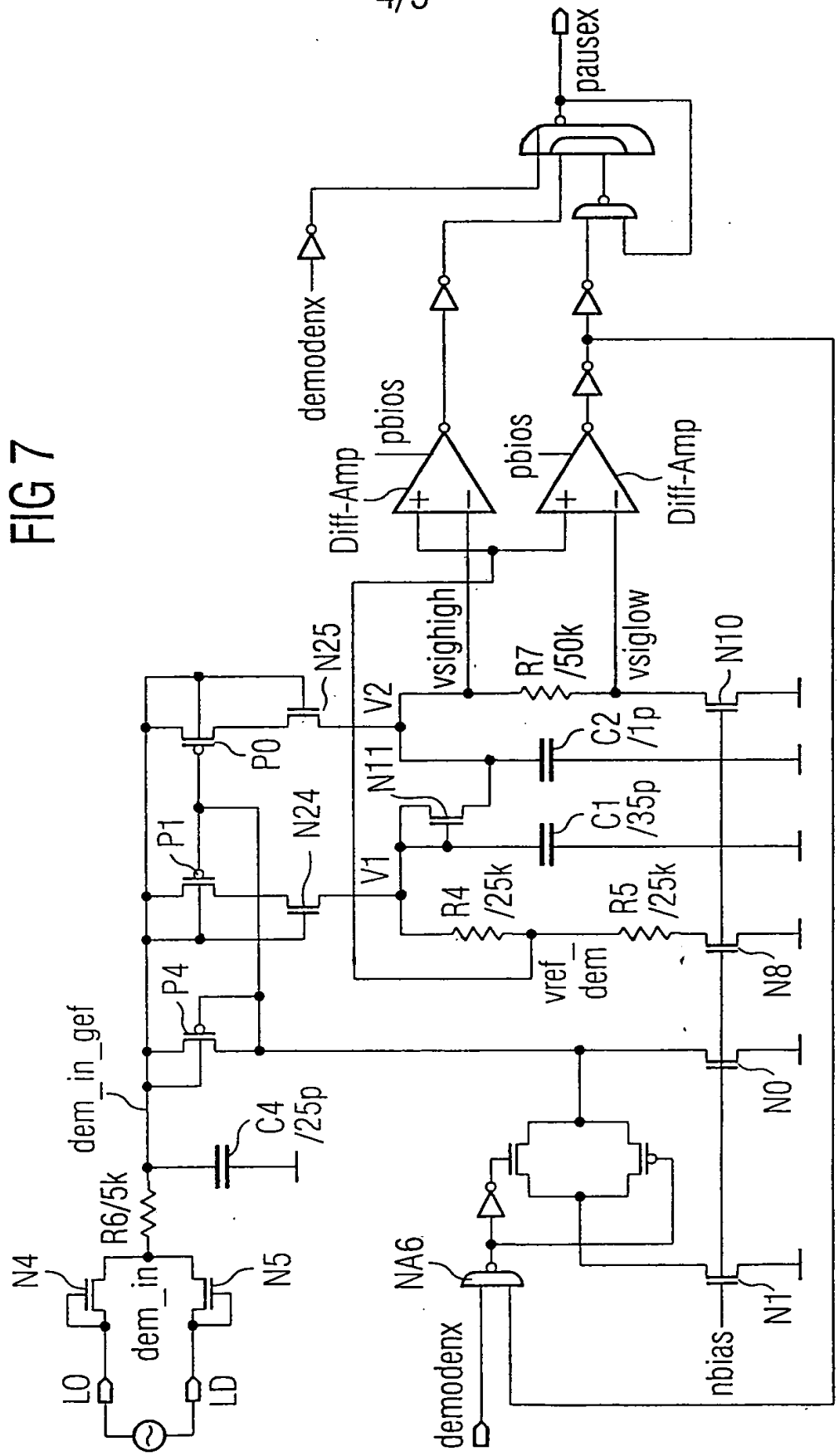

| UHF |
V
S
Vsighigh
Vref
Vsiglow
t1
t2
t

FIG 7


pausex
demodenx
pbios
Diff-Amp
pbios
Diff-Amp
vsighigh
R7
/50k
vsiglow
N10
N25
V2
C2
/1p
P0
N11
C1
/35p
P1
N24
V1
R4
/25k
R5
/25k
vref_
dem
N8
P4
dem_in_gef
N0
C4
/25p
R6/5k
N4
N5
dem_in
L0
LD
NA6
demodenx
nbias
N1

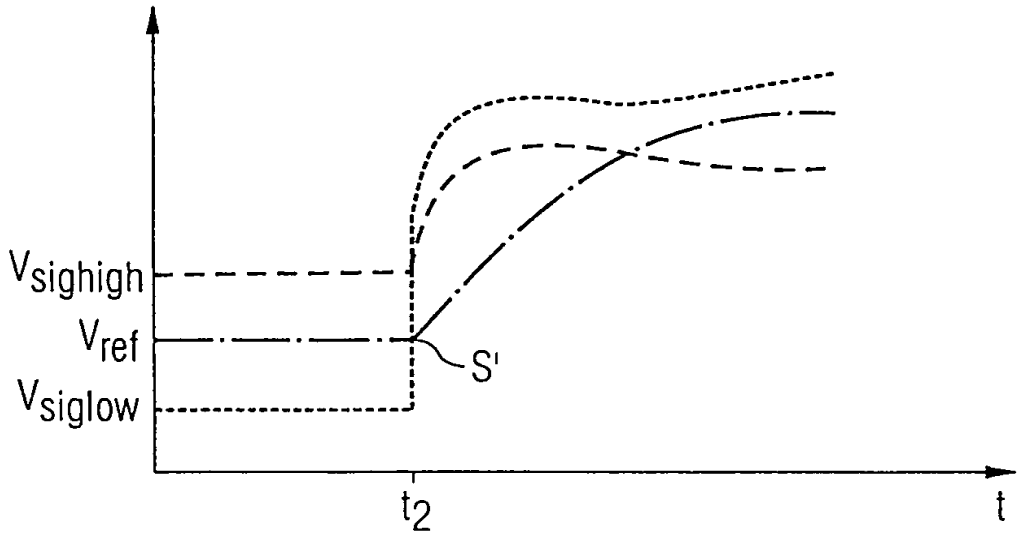



FIG 8